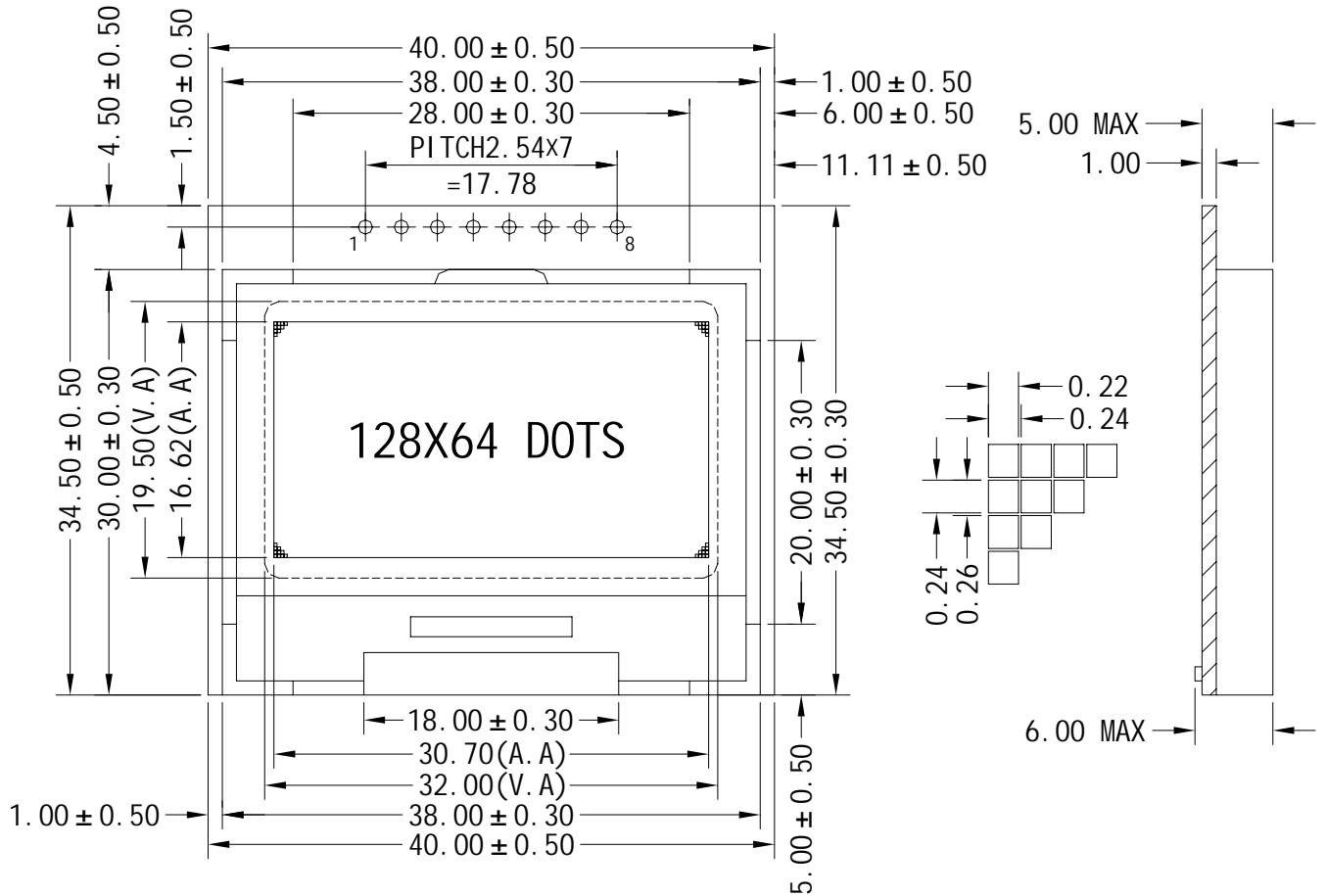


**DIMENSIONAL DRAWING**



**INTERFACE PIN CONNECTIONS**

1	2	3	4	5	6	7	8
/CS	/RES	A0	SCI	SI	VDD	VSS	BLA

**BLOCK DIAGRAM**

